

Curriculum Vitae



Bharti mehra

Assistant Professor in Electronics & Communication department
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OBJECTIVES:

To work in a challenging and stimulating environment with dedication and innovation, leading to rewarding and growth oriented career.

ACADEMIC DETAILS:

Qualification	Aggregate	Year of passing	Specialization/ Discipline/ Subject	University/board
Ph.D (Pursuing)	-	-	Design & Analysis of IoT enabled device for smart community	RGPV Bhopal
M.Tech	7.38 CGPA	2011	Embedded System	SOEX , DAVV Indore
B.E	69.00%	2008	Electronics and Communication	SATI Engineering College , Vidisha (M.P)
Diploma	62.63%	2005	Electronics and Communication	S.V Polytechnic College Bhopal
10 th Standard	65%	2001	PCM	M.P Board

WORK EXPERIENCE: (8 years)

1. Working in SATI Engineering College, Vidisha (From July, 2011 till now.)
2. 1 year Teaching Experience in Polytechnic College Bhopal (From 2008 to 2009)

TRAINING PROGRAM AND WORKSOP ORGANIZED:

1. Organized, 15 Days Hours training on 'C' Programming Language
2. Coordinator, 5 Days workshop on Proteus Simulation Software
3. Coordinator, Two week In-house Internship on Embedded Systems Design

ACHIEVEMENT:

Selected as Guest Faculty in NIT Bhopal Department of Electronics and Information Technology under SMDP in VLSO phase-II.

SUBJECT TAUGHT:

1. Microprocessor&Microcontroller
2. Internet of Things
3. Embedded Systems
4. Digital Circuits Design

RESEARCH AREA:

Embedded Systems Design and Internet of Things

TECHNICAL AND COMPUTER SKILLS:

- Programming Language: 'C' ,Embedded C,VHDL, ALP, Python
- Windows Packages: MS Office
- Tools: Keil μ -vision, CCS, MATLAB, Xilinx, Labview, Proteus.
- Hardware: 8051 Development board, ARM Development Board, AVR Development board, NI Elvis board.

PROJECT & THESIS WORK:

S.No	Title	Description	Duration
1	Collaboration of Robots	Design & Develop wireless communication between the robots using CC2500 wireless module.	1 year (M.Tech)
2	Traffic light controller	Traffic light controller using 8051 microcontroller	6 month (M.Tech)
3	Adaptive filter	Adaptive filter using MATLAB	6 month (B.E)
4	Object counter	Object counter using sensor	6 month (B.E)
5	FM Receiver	Receives FM signals	6 month (B.E)

THESIS GUIDE:

1. Efficient Sensor Node Determination

SEMINAR / WORKSHOP/CONFERENCE ATTENDED:

S.No.	Title	Duration	Institute where deputed
1.	NCEECS- 2010 National conference on Emerging Electronics and Computing Systems(Tutorial session)	29/03/2010- 01/04/2010 (3 DAYS)	SOEX- DAVV Indore
2.	NCEECS- 2010 National conference on Emerging	02/04/2010- 03/04/2010	SOEX- DAVV Indore

	Electronics and Computing Systems	(2 DAYS)	
3.	FDP	03/03/2012	SATI Vidisha
4.	ISTE Workshop on signal & Systems	02/01/2014 to 12/01/2014	SATI Vidisha
5.	Workshop on 'Embedded System Design using TIVA Platform'	03-05/09/2015	RGPV Bhopal
6.	National workshop on 'signal Processing'	04 to 06/12/14	SATI Vidisha
7.	Application of stepper motor in Robots	29/10/2015	CRISP Bhopal
8.	Workshop on 'Mathematical Principles in Engineering'	18 to 22/06/2016	CDEEP IIT Bombay
9.	Symposium on 'Signal Processing & Communication'	17 to 18/10/2016	IIT Gandhinagar
10.	Workshop on 'Mathematical method in science & Engineering'	22/12/2016	SATI Vidisha
11.	Workshop on 'Workplace Creativity and Time Management'	19/12/2016	SATI Vidisha
12.	Workshop on 'ISTE STTP on CMOS, Mixed Signal and Radio Frequency VLSI Design'	30/01/2017 to 04/02/2017	SATI Vidisha conducted by IITKG
13.	Workshop on 'Virtual Instrumentation'	05/03/2018 to 09/03/2018	SATI Vidisha
14.	Workshop on Microcontroller & It's Application'	17/03/2018 to 21/03/2018	SATI Vidisha
15.	TEQIP-III Summer Training Program on 'Active Learning Workshop'	11/06/2018 to 15/06/2018	IIT Bombay

HOBBIES:

Playing outdoor game, Meditation & listening music.

PERSONAL PROFILE:

Date of birth:	21-09-1985
Husband's name:	Brajesh Ku. Dhakad
Marital Status:	Married
Address :	H.No 293,Royal city ,Vidisha(M.P)
Languages Known:	English, Hindi
Nationality:	Indian

DECLARATION:

I hereby declare that the information furnished above is true with best of my knowledge.

Bharti Mehra