

**Samrat Ashok Technological Institute (Engineering. College) VIDISHA (M.P.)
(An Autonomous Institute Affiliated to RGPV, Bhopal)**

Proposed Scheme of Examination

B.Tech – V – Semester (ELECTRICAL ENGINEERING) for batch admitted in July 2018

S. No.	Subject Code	Category	Subject Name & Title	Maximum Marks Allotted						Contact Hrs.			Total Credits
				Theory Slot			Practical Slot			L	T	P	
				End Sem	Mid Sem. Exam	Quiz, Assignment	End Sem.	Term work Lab Work & Sessional	Total Marks				
1	EE-1851	DC-9	Control System	70	20	10	30	20	150	3	-	2	4
2	EE-1852	DC-10	Analog and Digital Electronics	70	20	10	30	20	150	3	-	2	4
3	EE-1853	DC-11	Power Electronics	70	20	10	30	20	150	3	-	2	4
4	EE-1854	DC-12	Electromagnetic Field Theory	70	20	10	-	-	100	3	1	-	4
5	EE-1855	OC-1	Generation of Electrical Power	70	20	10	-	-	100	3	-	-	3
6	EE-1856	DLC	Simulation Lab	-	-	-	30	20	50	-	-	2	1
7	EE-1857	DLC	Internship – II (Evaluation) (personality development)	-	-	-	50	-	50	-	-	4	2
8		DLC	60 hrs Internship in Industry or elsewhere Internship-III	To be evaluated in VII Semester									
			Total	350	100	50	170	80	750	15	1	12	22
9		MC	Disaster Management										
10		SL	MOOCs	-	-	-	-	-	-	-	-	-	-
11		NLC	Participation & Winning in National level competition										

MST: Minimum two mid semester tests to be conducted during Semester

L: Lecture

T: Tutorial

P: Practical

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