



Samrat Ashok Technological Institute (Engineering. College) VIDISHA (M.P.)
(An Autonomous Institute Affiliated to RGPV, Bhopal)
Scheme of Examination

B.Tech –For batch admitted in July 2018
Electronics And Communication

III Semester

S. No.	Subject Code	Category	Subject Name & Title	Maximum Marks Allotted						Contact Hrs.			Total Credits
				Theory Slot			Practical Slot			L	T	P	
				End Sem	Mid Sem. Exam	Quiz, Assignm ent	End Sem.	Term work Lab Work & Sessional	Total Marks				
1	EC1831	HSMC	Managerial Economics	70	20	10	-	-	100	3	1	-	4
2	EC1832	DC	Electronic Devices	70	20	10	30	20	150	3	-	2	4
3	EC1833	DC	Digital Logic Design	70	20	10	30	20	150	3	-	2	4
4	EC1834	DC	Network Analysis	70	20	10	30	20	150	3	-	2	4
5	EC1835	DC	Signal and Systems	70	20	10	-	-	100	3	-	-	3
6	EC1836	HSMC	Language Lab	-	-	-	30	20	50	-	-	2	1
7	EC1837	DLC	Evaluation of Internship – I completed at I year level & Seminar (personality development)	-	-	-	50	-	50	-	-	4	2
8		DLC	90 hrs Internship based on using various software's Internship – II	To be completed anytime during Third/Fourth semester. Its evaluation/ credit to be added in fifth semester.									
9			Total	350	100	50	170	80	750	15	1	12	22
10		MC	Ethics	Non Credit	-	-	-	-	-	-	-	-	-
11		SL	MOOCs	-	-	-	-	-	-	-	-	-	-
12		NLC	Participation & Winning in National level competition	-	-	-	-	-	-	-	-	-	-
			NSS/NCC	Qualifier									

MST: Minimum two mid semester tests to be conducted during Semester

L: Lecture

T: Tutorial

P: Practical



Samrat Ashok Technological Institute (Engineering. College) VIDISHA (M.P.)
(An Autonomous Institute Affiliated to RGPV, Bhopal)
Scheme of Examination

B.Tech –For batch admitted in July 2018
Electronics And Communication

III Semester

S. No.	Subject Code	Category	Subject Name & Title	Maximum Marks Allotted						Contact Hrs.			Total Credits
				Theory Slot			Practical Slot			L	T	P	
				End Sem	Mid Sem. Exam	Quiz, Assignm ent	End Sem.	Term work Lab Work & Sessional	Total Marks				
1	EC1831	HSMC	Managerial Economics	70	20	10	-	-	100	3	1	-	4
2	EC1832	DC	Electronic Devices	70	20	10	30	20	150	3	-	2	4
3	EC1833	DC	Digital Logic Design	70	20	10	30	20	150	3	-	2	4
4	EC1834	DC	Network Analysis	70	20	10	30	20	150	3	-	2	4
5	EC1835	DC	Signal and Systems	70	20	10	-	-	100	3	-	-	3
6	EC1836	HSMC	Language Lab	-	-	-	30	20	50	-	-	2	1
7	EC1837	DLC	Evaluation of Internship – I completed at I year level & Seminar (personality development)	-	-	-	50	-	50	-	-	4	2
8		DLC	90 hrs Internship based on using various software's Internship – II	To be completed anytime during Third/Fourth semester. Its evaluation/ credit to be added in fifth semester.									
9			Total	350	100	50	170	80	750	15	1	12	22
10		MC	Ethics	Non Credit	-	-	-	-	-	-	-	-	-
11		SL	MOOCs	-	-	-	-	-	-	-	-	-	-
12		NLC	Participation & Winning in National level competition	-	-	-	-	-	-	-	-	-	-
			NSS/NCC	Qualifier									

MST: Minimum two mid semester tests to be conducted during Semester

L: Lecture

T: Tutorial

P: Practical

Handwritten signatures and initials at the bottom of the page.