



**Samrat Ashok Technological Institute (Engineering, College) VIDISHA (M.P.)**  
(An Autonomous Institute Affiliated to RGPV, Bhopal)

Scheme of Examination

**B.Tech – For batch admitted in July 2018**

Electronics And Communication

**IV Semester**

S. No.	Subject Code	Category	Subject Name & Title	Maximum Marks Allotted						Contact Hrs.			Total Credits
				Theory Slot			Practical Slot			L	T	P	
				End Sem	Mid Sem. Exam	Quiz, Assignment	End Sem.	Term work Lab Work & Sessional	Total Marks				
1	EC1841	DC	Electromagnetic Theory	70	20	10	-	-	100	3	1	-	4
2	EC 1842	DC	Analog and Digital Communication	70	20	10	30	20	150	3	-	2	4
3	EC 1843	DC	Analog Circuits	70	20	10	30	20	150	3	-	2	4
4	EC 1844	DC	VLSI Circuit Design	70	20	10	30	20	150	3	-	2	4
5	EC 1845	BSC	Engineering Mathematics-III	70	20	10	-	-	100	3	-	-	3
6	EC 1846	DLC	Simulation Lab I	-	-	-	60	40	50	-	-	2	1
7	EC 1847	DLC	90 hrs Internship based on using various software's - Internship – II	To be completed anytime during Fourth semester. Its evaluation/ credit to be added in fifth semester.						-	-	-	-
<b>Total</b>				<b>350</b>	<b>100</b>	<b>50</b>	<b>150</b>	<b>100</b>	<b>750</b>	<b>15</b>	<b>1</b>	<b>8</b>	<b>20</b>
8		MC	Traditional Knowledge	-	-	-	-	-	-	-	-	-	-
11		SL	MOOCs	-	-	-	-	-	-	-	-	-	-
9		NLC	Participation & Winning in <b>National level competition</b>	-	-	-	-	-	-				

MST: Minimum two mid semester tests to be conducted during Semester

- Simulation Lab I: MATLAB

L: Lecture

T: Tutorial

P: Practical

*me* *BS* *Sh* *SR* *SR*



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