



Samrat Ashok Technological Institute (Engineering College) VIDISHA (M.P.)
(An Autonomous Institute Affiliated to RGPV, Bhopal)

Revised Scheme of Examination

B.Tech –For batch admitted in July 2018

Electronics And Communication

V Semester

S. No.	Subject Code	Category	Subject Name & Title	Maximum Marks Allotted						Contact Hrs.			Total Credits
				Theory Slot			Practical Slot			L	T	P	
				End Sem	Mid Sem. Exam	Quiz, Assignment	End Sem.	Term work Lab Work & Sessional	Total Marks				
1	EC 1851	DC	Wireless Communication	70	20	10	-	-	150	3	1	-	4
2	EC 1852	DC	Antenna and Wave Propagation	70	20	10	30	20	150	3	-	2	4
3	EC 1853	DC	Microprocessor and Interfacing	70	20	10	30	20	150	3	-	2	4
4	EC 1854	DC	Digital Signal Processing	70	20	10	30	20	100	3	-	2	4
5	EC 1855	OC-I	Open Category-I	70	20	10	-	-	100	3	-	-	3
6	EC 1856	DLC	Simulation Lab II	-	-	-	30	20	50	-	-	2	1
7	EC 1857	DLC	Internship – II (Evaluation) (personality development)	-	-	-	50	-	50	-	-	4	2
8		DLC	60 hrs Internship in Industry or elsewhere Internship-III	To be evaluated in VII Semester									
			Total	350	100	50	170	80	750	15	1	12	22
9		MC	Disaster Management										
10		SL	MOOCs	-	-	-	-	-	-	-	-	-	-
11		NLC	Participation & Winning in National level competition										

MST: Minimum two mid semester tests to be conducted during Semester

L: Lecture

T: Tutorial

P: Practical

*Simulation Lab II: LabVIEW Lab / DSP lab based on MATLAB

Open Category -I	(A)Control System	(B)Instrumentation and Measurement	(C)Digital Hardware Design
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Sandeep Patidar

Dr. Ashwin Kothari, VNIT Nagpur

Dr. P. K. Singhal, MITS Gwalior

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