



**Samrat Ashok Technological Institute (Engineering College) VIDISHA (M.P.)**  
**(An Autonomous Institute Affiliated to RGPV, Bhopal)**

**Proposed Scheme of Examination**

**B.Tech – V – Semester (E&I)**

**For batch admitted in July 2018**

S. No.	Subject Code	Category	Subject Name & Title	Maximum Marks Allotted						Contact Hrs.			Total Credits
				Theory Slot			Practical Slot			L	T	P	
				End Sem	Mid Sem. Exam	Quiz, Assignment	End Sem	Term work Lab Work & Sessional	Total Marks				
1	EI-1851	DC	Power Electronics	70	20	10	30	20	150	3	-	2	4
2	EI-1852	DC	Process Instrumentation II	70	20	10	30	20	150	3	-	2	4
3	EI-1853	DC	Microprocessors and Interfacing	70	20	10	30	20	150	3	-	2	4
4	EI-1854	DC	Digital Signal Processing	70	20	10	-	-	100	3	1	-	4
5	EI-1855	DC	Analog & Digital Communication	70	20	10	-	-	100	3	-	-	3
6	EI-1856	DLC	Lab VIEW Programming	-	-	-	30	20	50	-	-	2	1
7	EI-1857	DLC	Internship – II (Evaluation) (personality development)	-	-	-	50	-	50	-	-	4	2
8	EI-1858	DLC	60 hrs Internship in Industry or elsewhere Internship-III	To be evaluated in VII Semester									
			<b>Total</b>	350	100	50	150	100	750	15	1	12	22

MST: Minimum two mid semester tests to be conducted during Semester

L: Lecture

T: Tutorial

P: Practical

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