


Samrat Ashok Technological Institute (Engineering, College) VIDISHA (M.P.)
(An Autonomous Institute Affiliated to RGPV, Bhopal)
Proposed Scheme of Examination B.Tech VI Semester (E&I)
For batch admitted in July 2018


S. No.	Subject Code	Category	Subject Name & Title	Maximum Marks Allotted										Contact Hrs.			Total Credits
				Theory Slot					Practical Slot					L	T	P	
				End Sem	Mid Sem. Exam	Quiz, Assign ment	End Sem	Term work Lab Work & Sessional	Total Marks								
1	EI-1861	DC	Control System Engineering	70	20	10	30	20	150	3	-	2	4				
2	EI-1862	DE	DE-1	70	20	10	30	20	150	3	-	2	4				
3	EI-1863	DE	DE-2	70	20	10	30	20	150	3	-	2	4				
4	EI-1864	DE	DE-3	70	20	10	-	-	100	3	-	-	3				
5	EI-1865	OC	OC-2	70	20	10	-	-	100	3	-	-	3				
6	EI-1866	DLC	Minor Project	-	-	-	50	50	100	-	-	4	2				
7	EI-1867		90 hrs Internship in Industry or elsewhere Internship-III	To be evaluated in VII Semester													
			Total	350	100	50	140	110	750	15	-	10	20				
		NLC	Participation & Winning in National level competition	-	-	-	-	-	-	-	-	-	-				
			Entrepreneurship	-	-	-	-	-	-	-	-	-	-				
		SL	MOOCs	-	-	-	-	-	-	-	-	-	-				

MST: Minimum two mid semester tests to be conducted during Semester L: Lecture T: Tutorial P: Practical

	DE-1	DE-2	DE-3	OC-2
A	Medical Instrumentation	Microcontrollers	Data Structure	Optical Instruments and Sensors
B	Electromagnetic Theory	Peripherals & Interfacing	Filter Design & Simulation	Probability and Stochastic Process
C	High Frequency Engineering	Telemetry	PLC & SCADA	Statistical Signal Processing



Dr. Ashok Mishra, Vice Registrar



Dr. J.V. Ogale
Head, Dept. of Electronics
S.A.T.I. Vidisha