

Curriculum Vitae

Dr. Shilpa Datar
Assistant Professor
Department of Electronics Engineering
Samrat Ashok Technological Institute (Engineering College)
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PERSONAL INFORMATION

- Father's Name : Mr. P.K.Markhedkar
- Mother's Name : Mrs. Nalini Markhedkar
- Husband's Name : Dr. Ashutosh Datar
- Gender : Female
- Date of Birth : 5th May 1974
- Marital Status : Married

ACADEMIC PROFILE

- **Ph. D.** in Electronics and Instrumentation Engineering from Rajv Gandhi Proudhyogiki Vishwavidyalaya (RGPV), Bhopal (M.P.)
- **M.Tech** (Computer Science And Engineering) from S.A.T.I.Vidisha affiliated to RGPV Bhopal, with 83.57%, in the year 2010. (**Gold medal in University**)
- **B.E.** (Electronics and Instrumentation Engineering) from S.A.T.I., Vidisha affiliated to R.G.P.V Bhopal, with 81%, in the year 1996 (**Secured first position in Electronics and Instrumentation Discipline**).
- 12th Higher Secondary Examination (M.P. Board) with 86 % marks in 1992 (**26th position in state merit list**).
- 10th High School (M.P.Board) with 86.7 % marks in the year 1990 (**17th position in state merit list**).

WORK EXPERIENCE

S.No.	Designation	Department/ Institute	Duration
1.	Lecturer (Contract Basis)	Department of Electronics and Instrumentation Engg., Samrat Ashok Technological Institute, Vidisha(M.P.)	From October 1996 to January 2003
2.	Lecturer (Regular Basis)	Department of Electronics and Instrumentation Engg., Samrat Ashok Technological Institute, Vidisha(M.P.)	From January 2003 to January 2009
3.	Assistant Professor (Stage-1)	Department of Electronics and Instrumentation Engg., Samrat Ashok Technological Institute, Vidisha(M.P.)	From January 2009 to January 2014
4.	Assistant Professor (Stage-2)	Department of Electronics and Engineering Samrat Ashok Technological Institute, Vidisha(M.P.)	From January 2014 till date

PUBLICATIONS

- **International Journals**

1. *Implementation of Iterative Weiner Filter in Restoration of Color Images*, Shilpa Datar, Yogendra K. Jain, International Journal of Engineering Sciences, vol.1, n0:1, 2010, pp: 73-81.
2. *Quantized coefficient F.I.R filter for the design of Filter Bank*, Rajeev Singh Dohare, Prof. Shilpa Datar, International Journal of Engineering Trends And Technology (IJETT), Vol. 4, no: 8, August 2013, pp. 3271-3277.
3. *Image Restoration Based on Deconvolution by Richardson Lucy Algorithm*, Madri Thakur , Shilpa Datar, International Journal of Engineering Trends and Technology (IJETT), Vol.14(4),161-165 Aug 2014. ISSN:2231-5381.
4. *Performance Evaluation of Medical Image Compression Using Discrete Cosine and Discrete Wavelet Transform*, Anupreksha Jain, Shilpa Datar, International Journal of Engineering Trends and Technology (IJETT), Vol. 14 (6), 280-285, Aug 2014. ISSN:2231-5381
5. *Image Restoration Using Wavelet based Image Fusion*, Mohini Sharma, Prof. Shilpa Datar, International Journal of Engineering Trends and Technology (IJETT), Vol.15 (1),35-38 Sep 2014. ISSN:2231-5381.
6. *Design of direction oriented filters using McClellan Transform for edge detection*, Sujata Maurya, Ashutosh Datar, Shilpa Datar, International Journal of Current Engineering and Technology, Vol. 6(2), 446-450 April 2016, ISSN: 2347-5161.
7. *Application of image fusion techniques on Medical images*, Richa Gautam, Shilpa Datar, International Journal of Current Engineering and Technology, Vol. 7(1), 161-167, Feb 2017, ISSN: 2347-5161
8. *Implementation of Viola-Jones Algorithm Based Approach for Human Face Detection*, Laxmi Narayan Soni, Ashutosh Datar, Shilpa Datar, International Journal of Current Engineering and Technology, Vol. 7(5), pp: 1819-1823, Sep/Oct 2017, ISSN: 2347-5161.
9. *Viola Jones Algorithm Based Approach for Face Detection of African Origin People and Newborn Infants*, Laxmi Narayan Soni, Ashutosh Datar, Shilpa Datar, International Journal of Current Engineering and Technology, Vol. 51(2), pp : 75-81, Sept. 2017, ISSN: 2231-2803.
10. *Recognition of License Number Plate using a Template Matching Technique*, Poorvi Hedau , Shilpa Datar and Ashutosh Datar, International Journal of Current Engineering and Technology, Vol.7(5), pp: 1901-1905, Sept/Oct 2017, ISSN: 2347-5161.

11. *Application of nature inspired evolutionary algorithm optimization techniques in design of cosine modulated filter banks*, Shilpa Datar, Alok Jain, *Advanced Science, Engineering and Medicine*, vol. 11, no. 8, pp.766-774, August 2019.
12. *Design And Performance Analysis of ECG Data Compression Using Convolved Window Based Cosine Modulated Filter Bank*, S.Datar, A.Jain, *Journal of Engineering Science and Technology (JESTEC)*, vol. 15, no.5, pp. 3449 – 3464, October 2020.

- **International and National Conferences**

1. *An intelligent system for human chromosome classification*, Ashutosh Datar, N. Ganesh, SJ Kher, S.Datar, *National Symposium on Instrumentation (NSI-28)*, GB Pant University, Pantnagar, Uttaranchal, 3-5 Nov, 2003 India
2. *Geometrical feature based human chromosome classification using MLP*, Ashutosh Datar, N. Ganesh, SJ Kher, S.Datar, *Nat. conf. on Automatic control & Instrumentation (ACI-2004)*, St. Joseph's college of Engineering Chennai (TN) India.
3. *Pseudo-coloring of Medical Images using Parametric equations with reserved brightness*, Ashutosh Datar & Shilpa Datar, *National Conf. on Biomedical Engineering (NCBME 2008)* October 2007 MIT, Manipal (Kar.) India.
4. *Deblurring and Restoration of Images using Iterative Wiener Filter*, Shilpa Datar, Yogendra K. jain, and Ashutosh Datar, *Nat. conf. on Emerging trends in Engineering and Sciences (NC2010)*, SATI (Engg.) Vidisha M.P.
5. *An Improved and Efficient Thresholding Scheme for Subband Signals in ECG Data Compression*, Ashutosh Datar, Shilpa Datar and Alok Jain, *Proceedings of the International Conference on Communications, Networking and Signal Processing (ICCNASP-2013)*, VIT University, Vellore, India. 19-21, September 2013.pp: 197-199.
6. *Performance evaluation of high SLFOR fixed windows in ECG signal compression using optimized Cosine modulated filterbank*, Shilpa Datar, Ashutosh Datar, and Alok Jain, *Proceedings of the International Conference on Innovative trends in Electronics Communication and Applications' 13 (ICIECA 2013)*, 27-29 December 2013,ASDF, Pondicherry,India,pp:128-135.(<http://edlib.asdf.res.in/2013/icieca/ICIECA0016.pdf>)
7. *Application of Convolution Windows in Design of Filterbank With Reduced Aliasing Error*, Shilpa Datar, Alok Jain, *IEEE 2015 International Conference on Signal and Image Processing Applications (ICSIPA 2015)*, Kuala Lumpur, Malaysia, 19-21October 2015. doi: 10.1109/ICSIPA.2015.7412157
8. *Design of filterbanks based on convolved Blackman window for minimization of aliasing error*, Shilpa Datar, Alok Jain, *International Conference on Signal Processing (ICSP2016)*, Samrat Ashok Technological Institute (Engg. college),Vidisha (M.P.), 7-9 November 2016.

List of SDP/STTP/FDP/WORKSHOP attended

1. 2 Week AICTE sponsored Staff Development Program on *“Nanoelectronics,”* in the Department of Electronics And Communication Engg., SIRT, Bhopal, from 13th June to 25th June 2011.
2. 2 week AICTE sponsored Staff Development Program on *“Current Trends in Signal Processing”*, from 16th to 28th September 2011, at Department of Electronics and Instrumentation Engg. ,S.A.T.I, Vidisha (M.P.)
3. 1 day Faculty Development Program on *“ERP-SAP”*, conducted by Tata Consultancy Services.
4. 1 week TEQIP sponsored Short Term Training Program on *“Evolutionary Computing and Simulation Techniques in Engineering Applications”*, (ECSTA-2013), from 1st July to 5th July 2013 at Department of Electrical Engineering, MANIT,Bhopal.
5. 2 week ISTE workshop on *“Signals and Systems”*,conducted by Indian Institute of Technology,Kharagpur,from 2nd January to 12th January 2014, under NMEICT (MHRD),
6. 1 week Faculty Development Programme (FDP) under TEQIP-II, on *“Self Management and Communication”*, from 2-6 June 2014, at the Department of Humanities,SATI,Vidisha (M.P.).
7. 1 week Short Term Course on *“Advances in VLSI Signal Processing”*, from 2- 6 January 2015, at Department of Electrical and Electronics & Communication Engg., IIT Kharagpur, West Bengal.
8. 3 days *“National Workshop on Signal Processing”*, organized from 4-6 December 2014, at SATI Vidisha.
9. 1 week Short Term (GIAN) Course on *“Theory and Applications of Wavelets and Framelets”*, from 28 December 2017 to 4 January 2018, at Department of Mathematics, Indian Institute of Technology (IIT), Indore (M.P.).
10. 1 week Summer Training Course on *“Active Learning”*, from 11-15 June 2018, at Indian Institute of Technology (IIT) Bombay, Mumbai.
11. 1 week Teachers’ Training Program (TTP) on *“Internet of Things and its applications,”* 26th – 30th March 2019, Department of Information Technology, RGPV, Bhopal.

UNDERTAKING

I hereby declare that above written particulars are true to the best of my knowledge and belief.

Date: 15-05-2023

(Dr. Shilpa Datar)

Place: Vidisha (M.P.), India