

#### Samrat Ashok Technological Institute (Engineering. College) VIDISHA (M.P.) (An Autonomous Institute Affiliated to RGPV, Bhopal) Scheme of Examination

## B. Tech -For batch admitted in July 2018

**Electronics And Communication** 

## III Semester

S.	<u> </u>			Maximum Marks Allotted						Cont	Total Credits		
No.				Theory Slot Practical Slot									
	Subject	C 4	Subject Name & Title	End Sem	Mid	Quiz,	End	Term work	Total	L	T	P	
	Code	Category	Subject Name & Title		Sem.	Assignm	Sem.	Lab Work &	Marks				
	day year				Exam	ent		Sessional					
	= 01001	TIGN (C	M	70	20	10	-	_	100	3	1	-	4
<u>l</u>	EC1831	HSMC	Managerial Economics	70	20	10	30	20	150	3	-	2	4
2	EC1832	DC	Electronic Devices	70	20	10	30	20	150	3	† <del>-</del>	2	4
3	EC1833	DC	Digital Logic Design	70	20	10	30	20	150	3	_	2	4
4	EC1834	DC	Network Analysis		20	10	-		100	3	+_	-	3
5	EC1835	DC	Signal and Systems	70			30	20	50		+-	2	1
6	EC1836	HSMC	Language Lab	-	-	-		20	50		+-	4	2
7	EC1837	DLC	Evaluation of Internship – I	-	-	-	50	-	. 30	-	-	7	
			completed at I year level & Seminar										l
			(personality development)				<u></u>		1	111.		11-	: CAL
8		DLC	90 hrs Internship based on using	To be completed anytime during Third/Fourth semester. Its evaluation/cr							o be	added	. in 111111
			various software's Internship – II	semester.								T	
9			Total	350	100	50	170	80	750	15	1	12	22
10		MC	Ethics	Non	-	-	-	-	-	-	-	-	_
10		,,,,,		Credit									_
11		SL	MOOCs	-	-	-	-	-	-	-	-	-	
12		NLC	Participation & Winning in National	-	-	-	-	-					
~-			level competition								$\perp$	<del> </del>	<del> </del>
			NSS/NCC	Qualifier								<u></u>	L

MST: Minimum two mid semester tests to be conducted during Semester

L: Lecture

T: Tutorial

P: Practical

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