

Samrat Ashok Technological Institute (Engineering. College) VIDISHA (M.P.) (An Autonomous Institute Affiliated to RGPV, Bhopal) Revised Scheme of Examination

B. Tech -For batch admitted in July 2018

Electronics And Communication

VI Semester

S. No.		Category	Subject Name & Title	Maximum Marks Allotted						Contact Hrs.		cs.	Total
	Subject Code			Theory Slot			Practical Slot						Credits
				End Sem	Mid Sem. Exam	Quiz, Assignm ent	End Sem.	Term work Lab Work & Sessional	Total Marks	L	T	Р	
1	EC 1861	DC	Data Communication and Computer Networks	70	20	10	30	20	150	3	-	2	4
2	EC 1862	DE	Elective-I	70	20	10	30	20	150	3	-	2	4
3	EC 1863	DE	Elective-II	70	20	10	30	20	150	3	-	2	4
4	EC 1864	DE	Elective-III	70	20	10	-	=	100	3	-	-	3
5	EC 1865	OC-II	Open Category-II	70	20	10	-	-	100	3	-		3
6	EC 1866	DLC	Minor Project-I	-	-	-	50	50	100	-	-	4	2
7	EC 1867		90 hrs Internship in Industry or elsewhere Internship-III	To be evaluated in VII Semester									
			Total	350-	100	50	140	110	· 750	15	-	10	20
8		NLC	Participation & Winning in National level competition	-	-	-	-	-	-	-	-	-	
9			Entrepreneurship	-	-	-		-	-	-	-	-	-
10		SL	MOOCs	-	-	-	-	·	-	- D. D	-	-	-

P: Practical L: Lecture T: Tutorial MST: Minimum two mid semester tests to be conducted during Semester (C)Probability & Stochastic process (B)FPGA Architecture & Applications Elective-I (A) Optical Communication (C) Speech and Audio Processing (B) Computer Architecture (A) Microwave Theory and Techniques Elective-II (B)Nano Electronics and MEMS (C) Adaptive Signal Processing (A) Cellular and Mobile Communication Elective-III (C) Statistical Signal Processing (B)Biomedical Signal Processing (A)Embedded Systems Design Open Category-II



你一一

Dr. Ashwin Kothari, VNIT Nagpur

WmgRal_

Dr. P K Singhal, MITS Gwallor



(Dr. J V Ogale) Head, Dept, of Electronics SATI Vidisha