Scheme of Examination (Semester-V) Bachelor of Technology (B. Tech.) – Electronics and Communication Engg.
for Batch Admitted in Session: 2023-24 and Onwards

Subject Code	Subject Category	Subject Name	Theory				Practical			Total Marks	Contact Hrs. per week.			Total Credits
			ES	MS	Assignment	Quiz	ES	LW	Quiz		L	T	P	
EC-501	DC	Digital Signal Processing	60	20	10	10	30	10	10	150	3	0	2	4
EC-502	DC	Microprocessor and Microcontroller	60	20	10	10	30	10	10	150	3	0	2	4
EC-503	DC	Microwave Theory and Techniques	60	20	10	10	30	10	10	150	3	0	2	4
EC-504	DE	DE-1	60	20	10	10	--	--	--	100	3	1	0	4
OE-505	OE	OE-1	60	20	10	10	--	--	--	100	3	0	0	3
EC-506	DLC	Simulation Lab-II (LabVIEW)	--	--	--	--	30	10	10	50	0	0	4	2
EC-507	ILC	Internship-II (60 Hrs) Institute Level (Evaluation)	--	--	--	--	--	50	--	50	--	--	4	2
	ILC	Extracurricular Activities	It is a one credit per year activity to be endorsed in eight semester mark sheet											
Total			300	100	50	50	120	90	40	750	15	1	14	23

	DE -1	OE-1 (Open elective offers by Electronics Department for other Branches)
A	Wireless Communication	Signals and Systems
B	Computer Architecture	Microprocessor and Microcontroller
C	Power Electronics	CMOS Circuits Design

Abbreviations: ES-End Semester, MS-Mid Semester, LW-Laboratory Work/Assignment. (L: Lecture, T: Tutorial, P: Practical) BSC-Basic Science Course, ESC- Engineering Science Course, HSMC- Humanities Science and Management Course, MAC- Mandatory, Audit Course, AC- Audit Course, HEC- Holistic Education Courses: NSS/NCC/NSO, ITC- Information Technology Course, ILC-Institute Level Course, DC- Department Course, DE-Department Elective, OC- Open Course, DLC- Department Laboratory, PROJ- Project Work, VA-Value Added Course

BoS (B.Tech. EC) | Department of Electronics Engineering | May 28, 2025