SAMRAT ASHOK TECHNOLOGICAL INSTITUTE VIDISHA (M.P.)
Department of Computer Science and Engineering
Scheme of Examination
Bachelor of Technology (B.Tech) in Computer Science and Engineering

Semester-VI

S. No.	Subject Code	Category	Subject Name & Title	Maximum Marks Allotted						Contact Hrs.			Total Credits
				Theory Slot			Practical Slot						
				End Sem	Mid Sem. Exam	Quiz, Assignment	End Sem.	Term work Lab Work & Sessional	Total Marks	L	T	P	
1	CSE-1861	DC	Computer Networks	70	20	10	30	20	150	3	-	2	4
2	CSE-1862	DE	DE-1	70	20	10	30	20	150	3	-	2	4
3	CSE-1863	DE	DE-2	70	20	10	30	20	150	3	-	2	4
4	CSE-1864	DE	DE-3	70	20	10	-	-	100	3	1	-	4
5	CSE-1865	OC	OC-2	70	20	10	-	-	100	3	1	-	4
6	CSE-1866	DLC	Minor Project-I	-	-	-	50	50	100	-	-	4	2
7	CSE-1867		90 hrs Internship in Industry or elsewhere Internship-III	**To be evaluated in VII Semester**									
			Total	350	100	50	140	110	750	15	2	10	22
8		NLC	Participation & Winning in National level competition	-	-	-	-	-	-	-	-	-	-
9			Entrepreneurship	-	-	-	-	-	-	-	-	-	-
10		SL	MOOCs	-	-	-	-	-	-	-	-	-	-

MST: Minimum two mid semester tests to be conducted during Semester   L: Lecture   T: Tutorial   P: Practical

Dr. Kanak Saxena
Chairperson

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	DE-1	DE-2	DE-3	OC-2
A	Advanced Computer Network	Mobile Computing	Web Engineering	Network Security
B	Microprocessor	Embedded System	Advanced Computer Architecture	Computer Network
C	Soft Computing	Neural Network	Artificial Intelligence	Cyber Security & Forensic

MST: Minimum two mid semester tests to be conducted during Semester   L: Lecture   T: Tutorial   P: Practical

	DE-4	DE-5	DE-6	OC-3	OC-4
A	Wireless Network	Internet Technology	Android Programming	Artificial Intelligence	Microprocessor
B	Robotics	Parallel Computing	High Performance Computing	Data Science	Embedded Systems
C	Fuzzy Logic	Data Science	Machine Learning	Machine Learning	Robotics

Dr. Kanak Saxena
Chairperson