

Registration Fee details

Student: UG/PG/ Research Scholars	Rs. 800/-
Faculty (SATI Engg.)	Rs. 1000/-
Faculty (others Colleges)	Rs. 1200/-
Person from Industry	Rs. 1500/-

Note: No TD/DA shall be admissible to outstation candidates. The lodging and boarding for outstation candidates can be arranged at extra payment.

Participation in STTP is limited to 30 individuals. The STTP will be conducted in OFFLINE mode.

Payment Details

NEFT or Bank Transfer in favour of : "DIRECTOR SATI".

Bank Details

A/c Name: Director SATI
A/c Number: 30842554902
Bank Name: State Bank of India
IFSC code: SBIN0012193
Branch: SATI Vidisha

REGISTRATION LINK:

<https://forms.gle/X3Skh12gYc6pp4pJ7>

Or

Use the following QR Code for Registration



Last Date for registration:

08.10.2025, Wednesday

In case of any query, pl. contact,

STTP Coordinators

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CHIEF PATRON

Shrimant Maharaja Jyotiraditya M. Scindia
Hon'ble Chairman, MJES & BoG, SATI, Vidisha (M.P.)

PATRON

Dr. Laxmikant Markhedkar
Hon'ble Secretary, MJES & BoG, SATI Vidisha (M.P.)

Dr. Anil Kothari

Director General, Madhya Pradesh Council of
Science & Technology, Bhopal (M.P.)

Dr. Yogendra Kumar Jain

Director, SATI (Engg. College), Vidisha (M.P.)

ADVISORY COMMITTEE

Dr. Alok Jain, Dean (Academic)
Dr. Jetendra Parashar, Dean (R&D)
Dr. Jyotsna V Ogale, Dean (SW)
Dr. R.S. Bharadwaj, Sr. Principal Scientist, MPCST Bhopal
Mr. Vikas Shende, Principal Scientist, MPCST Bhopal
Dr. M.K. Rathore, Joint Project Director, MPCST Bhopal
Dr. Rajeev Jain, HoD, Dept. of Civil Engg
Dr. Pankaj Agarwal, HoD, Dept. of Mechanical Engg
Prof C.S. Sharma, HoD, Dept. of Electrical Engg
Dr. Ashutosh Datar, HoD, Dept. of Electronics Engg.
Dr. Kanak Saxena, HoD, Dept. of Computer Sc. & Engg
Dr. Shailendra Shrivastava, HoD, Dept. of IT
Dr. Shilpa Datar, HoD, Dept. of Cyber Security & IoT
Dr. Sunil Joshi, HoD, Dept. of Artificial Intelligence
Dr. Manoj Datar, HoD Applied Sciences Dept
Dr. Amitosh Singh, HoD, Dept. of Humanities
Dr. Prashant Tiwari, HoD, Dept. of Management Studies
Dr. Ashish Manoria, Coordinator (IQAC)

ORGANIZING COMMITTEE

Dr. Neelesh Mehra, Asst. Professor
Dr. D K Shakya, Asst. Professor
Prof. M L Jatav, Asst. Professor
Dr. Sweety Jain, Asst. Professor
Dr. Abhishek Jain, Asst. Professor
Prof. Niraj Kumar, Asst. Professor

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SAMRAT ASHOK TECHNOLOGICAL INSTITUTE

(Engg. College), Vidisha (M.P.)

www.satiengg.in



One-week
Short Term Training Program
(STTP)

on
**Embedded Systems
& IoT Devices**

(13th -17th October 2025)

Organized by



Department of Electronics
Engineering,
SATI (Engg. College),
Vidisha (M.P.)

In Association with



Madhya Pradesh Council of
Science and Technology,
Bhopal (M.P.)

ABOUT SATI

Samrat Ashok Technological Institute, Engineering College, one of the oldest premier institutes of the Central India region, was established by Late Maharaja Jiwaji Rao Scindia on November 1, 1960 with a donation from Gangajali Trust fund.

The Institute started with B.E. in Civil Engineering, Mechanical Engineering & Electrical Engineering. Now the Institute offers 13 full time Under Graduate programmes leading to degree in Bachelor of Technology & BBA and 8 full time Post Graduate Courses (5 ME/M.Tech., MCA, MBA And M.Sc. in Applied Chemistry) in the areas of Engineering, Science and Management.

It is an autonomous institute, which is Grant-in-Aid by Government of Madhya Pradesh and managed by the Maharaja Jiwaji Rao Education Society (MJES) chaired by Hon'ble Shrimant Maharaja Jyotiraditya M. Scindia. The college campus is spread over an area of 85 acres of lush green land with natural surroundings.

ABOUT ELECTRONICS ENGINEERING DEPARTMENT

The Department was established in the year 1985, keeping in view the tremendous development in the field of Electronics, Instrumentation and Communication Engineering. The first UG Programme in Electronics & Instrumentation Engineering was initiated with the intake of thirty students. This undergraduate programme was accredited during 2012, 2018, 2022 NBA (National Board of Accreditation). In the year 2000, another UG Programme in Electronics & Communication was also initiated with an intake of sixty seats.

At present, the Department of Electronics Engineering offers two UG Programmes:

- ✓ B.Tech in Electronics & Instrumentation Engineering (NBA Accredited)
- ✓ B.Tech in Electronics & Communication Engineering

The department is well equipped with modern up-to-date laboratory facilities. The faculty is well qualified with specializations in diversified areas.

ABOUT MPCST, BHOPAL

The Madhya Pradesh Council of Science and Technology (MPCST), based in State capital Bhopal, is an autonomous body under the Madhya Pradesh state government that promotes the use of science and technology for social and economic development. Established in 1981, its core mission is to identify areas where scientific innovation can address challenges like poverty, unemployment, and rural backwardness—especially among marginalized communities. The MPCST supports research and development projects, facilitates technology-based solutions, and helps institutions tap into the state's natural resources. It also works to spread scientific awareness through seminars, publications, and outreach programs, while advising the government on science policy and collaborating with national and state-level bodies.

ABOUT STTP ON EMBEDDED SYSTEM AND IOT DEVICES

The MPCST sponsored STTP, organised during **13-17 October, 2025**, is designed for faculty members, research scholars, and undergraduate/postgraduate students. This program aims to bridge the gap between foundational knowledge and cutting-edge applications in one of the most transformative areas of modern technology.



The Embedded systems and IoT (Internet of Things) devices are at the heart of today's smart innovations—from intelligent healthcare and precision agriculture to smart cities and industrial automation. As the world moves toward hyper-connectivity and real-time data-driven decision-making, understanding how embedded platforms interact with sensors, actuators, and cloud services has become essential.

This STTP will offer participants a hands-on, application-oriented experience, covering key topics such as microcontroller programming, sensor integration, communication protocols, edge computing, and IoT security. Through expert-led sessions and practical demonstrations, attendees will gain insights into both the theoretical foundations and real-world deployment of these technologies.

The importance of this training lies not only in its relevance to current industry trends but also in its potential to shape future innovations. As embedded intelligence becomes a core component of next-generation devices, professionals equipped with this knowledge will be better positioned to contribute to research, product development, and interdisciplinary collaborations.

PROGRAMME OUTLINE

STTP is expected to covers the following topics, through expert lectures, technical sessions, and Hands-on lab sessions:

- Introduction to IoT and Embedded systems
- Understanding the Sensors, actuators and Display devices.
- Programming of Arduino, ESP01, ESP 8266 Node MCU and ESP32 development boards.
- Sensor interfacing, Data acquisition, monitoring, and display.
- Programming using MicroPython and Embedded C
- IoT communication protocols
- Programming IoT devices, monitoring and control using cloud and apps.
- Data logging, Remote monitoring and control applications

RESOURCE PERSONS

- ✓ Mr. Dilip Makwe, Technical Consultant, Cloud4Things Technologies Pvt. Ltd. Noida U.P.
- ✓ Mr. Chandrabhan Patel, Scientist, Construction, Automation & Robotics (CA&R) Group, Central Building Research Institute (CBRI), Roorkee (UK).
- ✓ Mr. Gaurav Sharma, Training Officer, SSR Global Skills Park, Bhopal (M.P.)